Date:02/08/2023

Faculty Profile

Name:	Mrs. Meghana M N	
Current Designation:	Assistant Professor	
Date of Current Designation	16/07/2018	
Department	Electronics and Communication Engineering	
University Faculty Reg. No	4ADEC0009566	

Qualification:

Degree	Year	College and University
PG - M.Tech	2017	Vidyavardhaka College of Engineering, Mysore - VTU
UG - BE	2014	Coorg Institute of Technology, Ponnampet - VTU

Date of joining to the Institute: 16/07/2018

Details of Positions Occupied:

Designation	Duration		Department
	From	**To**	
Assistant Professor (ATME)	01/08/2017	15/06/2018	ECE
Assistant Professor (MITT)	16/07/2018	Till date	ECE

Details of Publication:

No. of papers published in National Conference:	-
No. of papers published in Inter-national Conference:	-
Number of International Journal:	-

Description of the paper and Reference
1.
2.
3.
4.

No of National Workshops / FDP attended: 03

Workshop Title	Venue	Date
Next Generation Wireless Networks for Autonomous Intelligent Communications	Sri Venkateshwara College of Engineering, Bengaluru	26-12-2022 - 30-12-2022
Introduction to Python Programming & its Applications	Visvesvaraya Technological University, Centre for PG Studies, VIAT, Muddenahalli, Chikkaballapur	13/03/2023 - 17/03/2023
Industrial Internet of Things (IIOT) and Transition to Internet of Brains (IOB)	Don Bosco Institute of Technology AICTE Training and Learning (ATAL) Academy	20/09/2021 - 24/09/2021

Responsibilities taken in college level:

Event	Positions	Year

Responsibilities taken in Department level:

Committee	Positions	Duration	
		From	To
IA coordinator		2020	Till date
Preparation of Academic calendar		2020	Till date
Student feedback coordinator		2020	Till date
Technical seminar coordinator		2018	Till date
Timetable coordinator		2020	Till date
Student Mentor	Mentor	2020	Till date

No of student projects carried out as a guide (UG):

Project Name & Descriptions	Year
IOT based anti-poaching alarm system for trees in forest and farm field	2019-20
Drone based antenna	2020-21
FPGA implementation of 32 bit Vedic multiplier and square architectures	2021-22
IOT based air and water pollution monitoring system	2022-23

No of subjects taken for UG students:

Subject Title	Year	Semester and branch	No. of students attended	No. of students passed	Pass percentage
Digital Electronics – 17EC34	2018-19	III	36	32	89%
Verilog HDL – 15EC53	2018-19	V	44	42	92%
Linear Integrated Circuits – 17EC45	2018-19	IV	36	34	94%
Digital System Design using Verilog – 15EC654	2018-19	VI	44	44	100%
Verilog HDL – 17EC53	2019-20	V	34	33	97%
Real Time Systems – 15EC744	2019-20	VII	42	42	100%
Digital System Design using Verilog – 17EC654	2019-20	VI	34	32	84%
Wireless Cellular Communication and LTE 4G Broadband – 15EC81	2019-20	VIII	42	42	100%

Course	Year	Semester			
Verilog HDL – 18EC56	2020-21	V	38	37	97%
Real Time Systems – 17EC744	2020-21	VII	34	34	100%
Digital System Design using Verilog – 18EC644	2020-21	VI	38	38	100%
Wireless Cellular Communication and LTE 4G Broadband – 17EC81	2020-21	VIII	34	34	100%
Verilog HDL – 18EC56	2021-22	V	38	36	95%
Real Time Systems – 18EC731	2021-22	VII	38	38	100%
Digital System Design using Verilog – 18EC644	2021-22	VI	30	28	94%
Wireless Cellular Communication – 18EC81	2021-22	VIII	38	38	100%
Verilog HDL – 18EC56	2022-23	V	38	36	95%
Real Time Systems – 18EC731	2022-23	VII	30	30	100%
Digital System Design using Verilog – 18EC644	2022-23	VI			
Wireless Cellular Communication – 18EC81	2022-23	VIII	30	29	97%

Personal Details:

Permanent Address: #F3, 1st floor, sunrise apartment, 7th cross, Krishnamurthy puram, Mysore - 570004
 Blood group: O+
Date of Birth: 24/09/1992
Father's name: Nagesh L
Mother' name: Shobha M
Nationality: Indian