


Name:	MS.BHAGYALAKSHMI V	
Current Designation:	Assistant Professor	
Date of Current Designation	10/11/2021	
Department	Electronics and Communication Engineering	
University Faculty Reg. No	4GWEC0001739	

Qualification:

Degree	Year	College and University
M.Tech	2009	SJCE, Mysore
B.E	2006	VVIET, Mysore

Date of joining to the Institute: 10/11/2021

Details of Positions Occupied:

Designation	Duration		Department
	From	To	
Assistant Professor	10/11/2021	Till date	ECE

Details of Publication:

No. of papers published in National Conference:

No. of papers published in Inter-national Conference:

Number of International Journal:

Description of the paper and Reference
1.
2.
3.
4.

No of National Workshops attended:

workshop Title	Venue	Date
“An Overview of Teaching Techniques in Basics of Electronics and Communication Engineering”	VTU Human Resource Development Centre (VTU - HRDC), Centre for PG Studies, VIAT, Muddenahalli, Chikkaballapur (Dist.) - 562101. (Online)	13 th to 17 th December 2021
Digital System Design and Modelling using Verilog	Vidya Vardhaka College of Engineering, Mysore. (Proxelera)	22/06/2022

Responsibilities taken in college level:

Event	Positions	Year
-	-	-
-	-	-

Responsibilities taken in Department level:

Committee	Positions	From	Duration	To
Result Analysis committee	Result Analysis Co ordinator	10/11/2021		Till date
Class committee	Class committee co ordinator	10/11/2021		Till Date

No of student projects carried out as a guide (PG):

Project Name & Descriptions	Year
“Smart Agriculture Fencing System Using IOT”	2021-22
“Interpretation and Translation of Sign Language using MATLAB”	2022-23

No of subjects taken for UG students:

Subject Title	Year	Semester and branch	No. of students attended	No. of students passed	Pass percentage
Basic Electronics & engineering	2021-22	I ‘A’	46	41	89%
Basic Electronics & engineering	2021-22	II ‘B’	45	44	97.7%
VLSI Design	2021-22	VII	39	33	84.6%
Basic Electronics & engineering	2021-22	I ‘C’	43	42	97.6%
Engineering Statistics & Linear Algebra	2021-22	IV	39	26	66.6%
Communication Lab	2021-22	VII	30	30	100%
Introduction to Python Programming	2022-23	I ‘C’	61	60	98.3%
Introduction to Python Programming	2022-23	I ‘D’	52	41	78.84%
Introduction to Python Programming Lab	2022-23	I ‘C’	61	61	100%
Introduction to Python Programming Lab	2022-23	I ‘D’	52	52	100%

Personal Details:

Permanent Address: 353/C, 2nd Main , 2nd
Cross, Kuvempunagar M Block, Mysore-
570023

Date of Birth: 01/05/1985
Father's name: Velumani K
Mother' name: Shanthy V
Nationality: Indian

Blood group: O +ve